

# Designing Superdirective Patch Antenna Array Using Metamaterial

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## Abstract

Microstrip patch antenna has certain drawbacks of low gain, narrow band, and surface wave losses. In this paper the solution method used is a patch array with SRR metamaterial, which reduces the mutual coupling between the patch elements, thereby improving the directive gain and reducing the surface waves. A rectangular microstrip patch antenna that meets the requirement of operation at (8.29 GHz), the proposed configurations are simulated and analyzed in Ansoft High Frequency Structure Simulator. The VSWR, radiation patterns for gain and S11 performance are used for the analysis of the final design. Feed point on the patch that gives a good match of 50 ohm, input impedance was found by a method of trial and error.

## 1. Introduction

A microstrip patch antenna has the advantages of low cost, light weight, and low profile planar configuration. However, they suffer from the disadvantage of low directive gain [1-2]. Gain improves as the number of patch elements increase, and simultaneously as the mutual coupling between the elements reduces. This paper presents the use of three patch elements in an array with an SRR which acts as a filter for the resonating frequency. The dielectric constants are usually in the range of ( $2.2 \leq \epsilon_r \leq 12$ ) [3-4]. This paper presents the use of transmission line method to analyze the rectangular micro strip antenna [5]. The operating frequency used for the Rectangular patch antenna array in this paper is 8.29 GHz. RMPA is characterized by its width W, length L and thickness h, as shown in Figure-1.

RMPA, which has been used in this paper, the length L of the patch is usually  $0.3333\lambda_0 < L < 0.5\lambda_0$ , where  $\lambda_0$  is the free-space wavelength. The patch is selected to be very thin such that  $t \ll \lambda_0$  (where t is the patch thickness). The height h of the dielectric substrate is usually  $0.003\lambda_0 \leq h \leq 0.05\lambda_0$ . The dielectric constant of the substrate ( $\epsilon_r$ ) is typically in the range  $2.2 \leq \epsilon_r \leq 12$ .

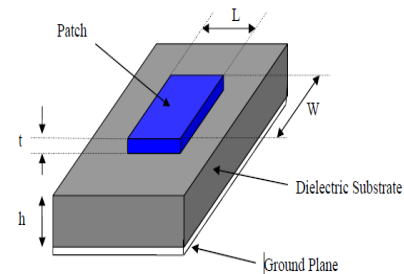


Figure 1. Structure of a rectangular microstrip patch antenna.

## 2. Design Consideration

We have designed an array of rectangular patch Antennas of the resonant frequency 8.29 GHz. We have employed a patch array antenna structure where we are using Rogers RT/duroid 5880(tm) having  $\epsilon_r=2.2$  as the dielectric. The first patch is fed via a microstrip line of 50 ohms impedance [8]. The array is the replication of the first patch. One side is substrate, backed with the ground on the other side, as can be seen in figure 1.

Microstrip antennas offer several tradeoffs that need to be considered [9]. Because they are manufactured with PCB traces on actual PCB boards, they can be vary, the three essential parameters for the design of microstrip patch antenna are: 1) Frequency of operation ( $f_0$ ): The resonant frequency of the antenna must be selected appropriately. 2) Dielectric constant of the substrate ( $\epsilon_r$ ). A substrate with low dielectric constant (2.2) is selected since it reduces fringing fields 3) Height of dielectric substrate (H): For the microstrip patch antenna the height of the dielectric substrate is critical since the antenna should not be bulky. The transmission line model will be used to design the antenna [7]. A microstrip line feed is to be used in this design. The center of the patch is taken as the origin and the coordinates give the feed point location ( $X_f, Y_f$ ):

### 2.1 Antenna Design Calculations:

The transmission line model will be used to design the antenna.

**2.1.1 Calculation of the Width (W):** The width of the microstrip patch antenna is given by equation (1)

$$W = \frac{c}{2f_o \sqrt{\frac{\epsilon_r + 1}{2}}} \quad (1)$$

Here taking Rogers RT/duroid 5880(tm) as the dielectric material used as substrate so,  $\epsilon_r$  as 2.2, by taking a operating Frequency, Velocity of light,  $c = 3 \times 10^8$  m / s, we get width of patch for our desired application as 14.30mm.

**2.1.2 Calculation of height of dielectric substrate (H):** The equation (2) to determine the height of the dielectric substrate is:

$$H = \frac{0.3c}{(2\pi f_o \epsilon_r)^2} \quad (2)$$

Here again taking Rogers RT/duroid 5880(tm) as the dielectric material used as substrate so,  $\epsilon_r = 2.2$ , Frequency of operation  $f_o$ , Velocity of light,  $c = 3 \times 10^8$  m/s. Taking these values we get height of dielectric substrate as 0.1025mm.

**2.1.3 Calculation of Effective dielectric constant ( $\epsilon_{eff}$ ):** Following equation (3) gives the effective dielectric constant as:

$$\epsilon_{eff} = \frac{\epsilon_r + 1}{2} + \frac{\epsilon_r - 1}{2} \left[ 1 + 12 \frac{h}{W} \right]^{-\frac{1}{2}} \quad (3)$$

We take the value of  $\epsilon_r$  as 2.2 because using Rogers RT/duroid 5880(tm) as substrate. Using the values of W and h as calculated above we find  $\epsilon_{eff} = 2.5$ .

**2.1.4 Calculation of the Effective length ( $L_{eff}$ ):** Equation below gives the effective length as:

$$L_{eff} = \frac{c}{2f_o \sqrt{\epsilon_{eff}}} \quad (4)$$

Again taking values  $\epsilon_{eff} = 2.5$ ,  $f_o$ ,  $c = 3 \times 10^8$ mm We get  $L_{eff} = 12.06$ mm. Calculation of the length extension ( $\Delta L$ ): Equation (5) below gives the length extension

$$\Delta L = 0.412h \frac{\left( \epsilon_{eff} + 0.3 \right) \left( \frac{W}{h} + 0.264 \right)}{\left( \epsilon_{eff} - 0.258 \right) \left( \frac{W}{h} + 0.8 \right)} \quad (5)$$

Substituting  $\epsilon_{eff}$ , W, and H and we get:  $\Delta L = 0.4042$ mm

**2.1.5. Calculation of actual length of patch (L):** The actual length is obtained by equation (6)

$$L = L_{eff} - 2\Delta L \quad (6)$$

Substituting  $L_{eff}$  and  $\Delta L$  and so we get:  $L = 11.25$ mm  
Calculation of the ground plane dimensions ( $L_g$  and  $W_g$ ): The transmission line model is applicable to infinite ground planes only. However, for practical considerations, it is essential to have a finite ground plane. It is known that similar results for finite and infinite ground plane can be obtained if the size of the ground plane is greater than the patch dimensions by approximately six times the substrate thickness all around the periphery [1]. Hence, for this design, the ground plane dimensions would be given as:

$$L_g = 6h + L = 16.014\text{mm}$$

$$W_g = 6h + W = 19.064\text{mm.}$$

**2.1.6. Determination of feed point location:** A microstrip line feed is to be used in this design. As shown in Figure the center of the patch is taken as the origin and the feed point location is given by the coordinates ( $X_f, Y_f$ ) from the origin. The feed point must be located at that point on the patch, where the input impedance is 50 ohms for the resonant frequency [6]. Usually it is found practically that feed point is found at one third of the length and width of the patch. And then to get the exact location of the feed point a trial and error method is used, i.e., to compare the return loss (R.L) and select the feed point where the R.L is allowable.

The parameters calculated above are optimized in order to obtain the results.

### 3. Simulation Results

For the simulation of results, Ansoft High Frequency Structure has been used. A patch antenna with length= 12mm, width=15.69mm and  $\epsilon_r = 2.2$  is designed, then the patch array designed is the replica of the single patch. A SRR is then inserted in the microstrip line to reduce the mutual coupling and thereby improve the directive gain.

It should be noted that lower the return loss, the better the antenna radiates. Figure 2 shows the S21 parameter of microstrip line with SRR inserted into it. It provides a peak isolation of -29dB. Figure 3 shows maximum dip at the resonant frequency, and provides return loss of -22.07 dB. Figure 4 shows that a VSWR  $\leq 2$  is achieved for patch antenna array. From figure 5, we observe that a directive gain of 10.03dB is achieved, by reducing the mutual coupling between the patch elements by the SRR. SRR acts as a filter for the resonant frequency. SRR reduces the surface waves and thereby improves the gain.

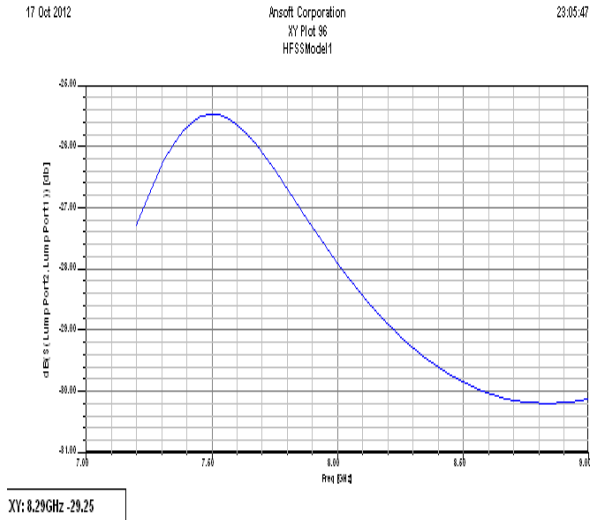


Figure 2. S12 parameter of microstrip line

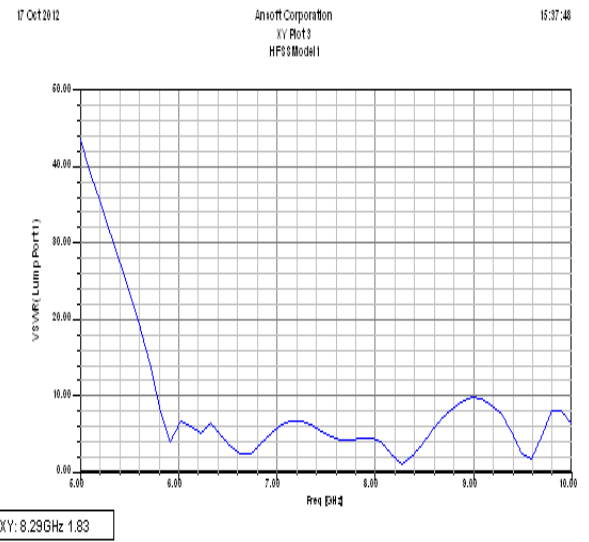


Figure 4. VSWR of patch antenna array with metamaterial

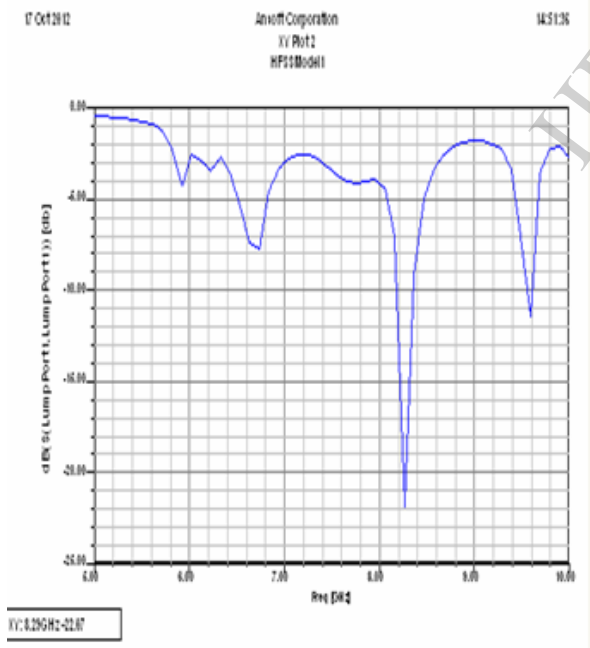


Figure 3. S11 parameter for patch antenna array with metamaterial

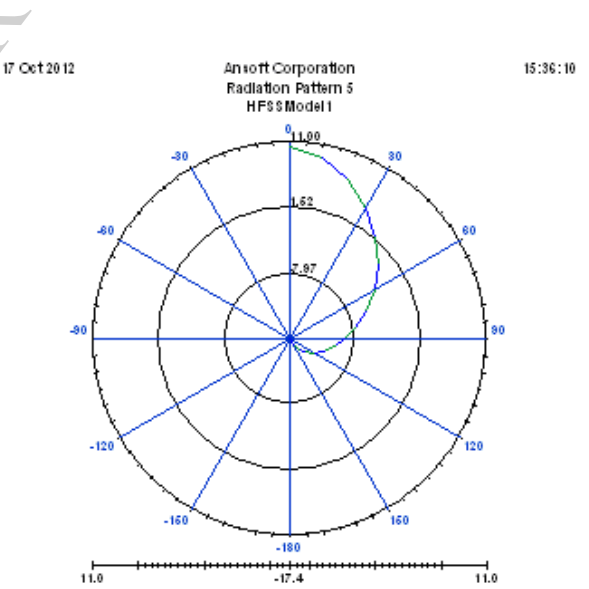


Figure 5. Radiation pattern for directive gain of patch antenna array with metamaterial

**Table 1.**Effect of patch parameters on antenna performance

Dielectric thickness (h mm)	Patch Specification (mm)	$f_o$ (GHz)	Return losses	Directive Gain
0.794	W=15.69 mm $\Delta L=0.4042$ $\epsilon_{eff} = 2.5$ L=12mm	8.29 GHz	-22.07 dB	10.03 dB

#### 4. Conclusion

The metamaterial resonator has been shown to be an effective insulator. Despite its small size it provided -29 dB of peak isolation. A physically small three-element patch array employing metamaterial insulators demonstrated superdirectivity. For substrate thickness of 0.794mm width 15.69 mm and length 12mm we get a directive gain of 10.08dB, which is much improved in comparison to a single patch antenna. We also know that suppressing mutual coupling as is provided by such insulators allows improvement in beam-steering and anti-jamming null-steering, and this is another reason to use metamaterial insulators in such arrays. This experiment has shown that metamaterial insulators are effective in the design of compact arrays, and it is expected that they can be found useful for numerous other applications.

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